ISSN- 2394-5125 VOL 7, ISSUE 19, 2020

PHOTOVOLTAIC DESIGN WITH HIGH GAIN DC-DC FULL BRIDGE CONVERTER

¹P Srividya Devi Associate Professor, *Department of Electrical and Electronic Engineering Gokaraju Rangaraju Institute of Engineering and Technology* Hyderabad, India <u>srividyadevi.p@gmail.com</u>

²V. Vijay Rama Raju Professor, *Department of Electrical and Electronic Engineering Gokaraju Rangaraju Institute of Engineering and Technology*, Hyderabad, India <u>vijayram_v@yahoo.com</u>

> ²M Rekha Assistant Professor, Department of Electrical and Electronic Engineering Gokaraju Rangaraju Institute of Engineering and Technology Hyderabad, India , <u>rmudundi@gmail.com</u>

Abstract

The grid-connected AC Module is an alternative for photovoltaic installations (PV). It has a PV panel and a micro-inverter that is connected to the grid. However, if a low-voltage source is employed, the dc-ac phase that is needed to connect to the Web grid needs a high-efficiency power conversion. These systems, also referred to as micro-inverters or built-in module inverters, have recently gained a lot of popularity. High-level DC-DC transformers have drawn a lot of attention recently thanks to their numerous uses in the field of renewable energy and other areas. Non-linear power plants employ DC-DC converters. It seeks to increase voltage gains with a premium DC-DC transformer in photovoltaic (PV) and renewable energy applications. For the four suggested topologies, the efficiency is assessed for the one-megawatt (MW) example.

Keywords: dc-dc converter, bridge, photovoltaic applications, cell, etc.

1. INTRODUCTION

The complete DC-DC bridge transformers are presented, and their topology and operation are clarified. The different methods of operation that the converter experiences and the appealing highlights of the new converter are highlighted and depicted; the proposed converter appeared in Figure 1 contains a full-pass four-switch S_1 , S_2 , S_3 and S_4 , L induction information, T_3 power transformer, optional D_2 and D_3 diodes, and Co_2 capacitor output. The primary part of the converter circuit is these segments. A network of inactive components includes C_1 and C_2 condensers (T_1 and T_2 transformers) and Dz diode. This unrelated circuit network is the same as the Z-source converter, which is not replaced by transformers, nor does the network go like a snubber to punch tension peaks which can somehow be done at any point where a turn is killed. This inactive snubber network then benefits from the overall profitability of the proposed topology.

ISSN- 2394-5125 VOL 7, ISSUE 19, 2020

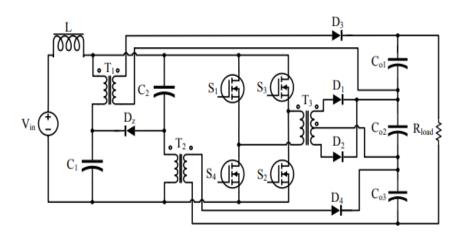


Figure 1: Proposed high-gain DC-DC converter

1.1 Modeling and examination of dc-dc converters

In non-linear, potent plants, DC-DC converters are used. The non-linearities occur mainly in nonturn-machine instances, storage components or parasitic components by switching power devices and uninvolved components. The advanced graph of system the essential methods in the numerical modelling and investigation of the DC-DC switching power converters The most commonly used methodology proposed by Brook and Cuk (1977) is a low sign examination based on the state-specific space average, circuit averaging, and the model of PWMs. The low-frequency reaction of such plants was determined to represent efficient and straightforward relations as far as the circuit sections were concerned. The Center creek and the Cuk 1977 examined the above technique by presenting the customary DC-DC converters, the State Space Averages Model (SSAM). The SSAM is one of the most acclaimed tools in power converter breakdown and design. SSAM implies that switching converters show little signage to the invariant conditions of a bunch of direct time. The switching converter's operational ratio as a variable structure controls each state condition (VSC). Subsequently, the frame matrices dependent on the duty rate d are measured in the middle of a model. The accuracy of the average strategy depends on the suspicion that there is little output and the average output rate is much less than the switching frequency. Fortunately, in the majority of switching converters, these assumptions are valid.

2.Z-SOURCE CONVERTER TOPOLOGIES

A Z-source converter does not have 18 inherently the same voltage as the conventional one. The distinguishing element is the inclusion of inducers, condensers and diodes in the information side and their familiarity with rising or down voltage. How regularly the converter is permitted to enter a shooting-through mode is the measure of venturing up or down. Since this mode is like the shooting mode of a current nutritional converter, which allows the voltage to reach in this kind of converter, the higher the recurrence of such shooting modes in a Z-source converter, the higher the voltage will progress.

Power request dramatically increments in the different fields because of new specialized innovation in multiple areas during these impending days; the need to use environmentally friendly power sources turns into a well-known issue because of lack of non-sustainable resources, simple accessibility and

ISSN- 2394-5125 VOL 7, ISSUE 19, 2020

minimal effort inexhaustible resources. These sources are commonly linked with various electrical converters such as rectifiers, inverters, choppers, voltage control systems, etc. The traditional sources include coal, diesel, water, fuel generally used in the energy transformation, etc. If an environmentally friendly power occurs, the output power generator is dc, so air conditioning or dc can be replaced. Two usual primary converters are available; a current source converter and a voltage source converter.

2.1 Conventional converters

□ Voltage Source Converter: A Dc voltage source, including a battery, condenser, sustainable source or diode correcter, was used in the Conventional V-source converter Fig-2 shows the usual voltage source topology; it includes a dc-voltage source connected to a condenser. The DC-Connect condenser used in VSI is used to operate as a low-impedance voltage source. Low power applications are fundamental to the design of a voltage source converter. However, the V-source converter has several disadvantages, for instance, an additional dc-dc network necessary to venture output voltage, the problem of shooting due to EMI noise, and the converter's reliability and an output LC filter required to filter the output provided control complications. The equal condenser also cares more about the power to the fault. It is risky.

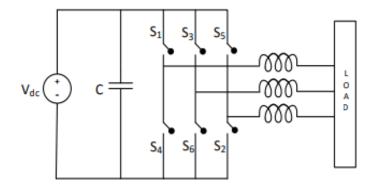


Figure 2: Conventional Voltage Source Topology

□ **Current Source Converter:**The standard I-source converter consists of an extensive DC inductive system, which is supplied with sources such as batteries, energy components and diode converters. The I-source converter is a dc-air conversion lift, as the info tension is not precisely the output tension. For ac-dc power change, the I-source converter is the buck. Due to the open circuit issue, the I-source converter generates EMI noises. Fig 3: shows the current topology of the traditional source.

ISSN- 2394-5125 VOL 7, ISSUE 19, 2020

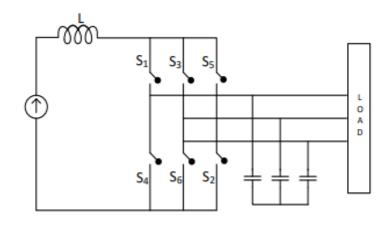


Figure 3: Conventional Current Source Topology

3.HIGH GAIN AND HIGH POWER DC-DC CONVERTER FOR PHOTOVOLTAIC APPLICATION

The photovoltaic (PV) module-coordinated converter (MIC) system is the vital technology for creating power utilizing sunlight based energy. Each PV module has its power conversion system. The power conversion system produces the most extreme violence from the PV module. To make the PV MIC system economically practical, high effectiveness and ease power conversion plan should be created. For the most part, the PV module has a low-voltage trademark. The low voltage (dc voltage of PV module) should be changed into a high dc voltage to convey electric power into the network. Therefore, a high-voltage gain dc-dc converter is required. The switch voltage stress of the coordinated course boost converter expands the conduction misfortunes and decreases the effectiveness of the converter. A converter with a coupled inductor can undoubtedly improve the voltage gain by raising the turn's ratio of the coupled inductor. Be that as it may, it has a substantially consistent state inductance. The huge constant state inductance builds misfortunes and consequently lessens the general productivity. Furthermore, the converter format is unpredictable, and the design methodology is likewise moderately mind-boggling.

3.1 Efficiency Estimation in High Power Applications

The efficiency is assessable for the given 1-MW model in this segment of the four proposed topologies. As already indicated, it is assumed that inducers parasite obstruction rL and IGBT opposition rs are the parts of the general efficiency. The suspicions in question are in line with the presumptions for genuinely high-power use; to minimize such a misfortune, the system should utilize software and high-quality segments. Regarding the consideration of parasitic elements, internal blockage of inductors per unit is expected. When Cuk or SEPIC converters work in booster mode, the current of the inductor is noted by a factor that is ideally equivalent to the conversion ratio rather than the output current. Therefore, the cross-sectional region of curls for the input inductor is dependent on being more prominent and therefore less blocking per unit.

Consequently, the parasite blockage inductor output is more notable. The suspicion is that rL1=0.0005 up, whereas rL2=0.001 up is based on the different topologies. The on-obstruction of IGBT is

ISSN- 2394-5125 VOL 7, ISSUE 19, 2020

nevertheless evaluated as 1.9 mm of the IGBT datasheet "5SNA 0750G650300." This value is used to assess the comparable IGBT string serial/parallel state obstruction in this section. The efficiency evaluation for the four unique topologies proposed is summarized in Table 1.

Converter	Stage				Estimated η %
Туре		rs(mΩ)	rL1(PU)	
				rL2(PU)	
Cascaded Cuk	Stage 1	2.85	0.000 5	0.001	91.5 %
	Stage 2	51			
Cascaded SEPIC	Stage 1	2.85	0.000 5	0.001	91.6 %
	Stage 2	51			
Series Cuk	Stage 1 & 2	28.5	0.000 5	0.001	95.8 %
	Buck-Boost	5.7	0.001	N/A	-
Series SEPIC	Stage 1 & 2	28.5	0.000 5	0.001	95.9 %
	Buck-Boost	5.7	0.001	N/A	

Table 1: Efficiency Estimation of the 1-MW example for the different topologies

The efficiency assessments are determined at the previously identified working focus areas, which are usually 0,9, showing a potential that, in any case, efficiencies of up to 90 per cent can be theoretically achieved for such a high operating cycle. Although cascade converters are superior to gain, Table 1 shows that for high-performance applications requiring a high converting ratio, the efficiency applies to a serial partnership. Therefore, a trade-off is established for these applications between these two borders. However, the assessed efficiency that depends on the evaluated values can be further enhanced by a proper choice of inductors for each stage. The similitude between Cuk and SEPIC is subsequently evident when the key factors are rL and rs. In this way, in high power applications, given the differences mentioned above, it is common for both converters to be similar in performance.

Furthermore, the high power situation can be extended to include a more exhaustive execution. For instance, the neighbourhood of several 1-MW ranches to be connected to the AC network through DC accesses is a hypothetical situation that can be considered. Then, through the proposed associations, the output of these ranches is increased to the DC-Link voltage. The accumulated DC power is

ISSN- 2394-5125 VOL 7, ISSUE 19, 2020

consolidated at the central station, which is then altered to AC and maintained within the framework. The required electrical seclusion is achieved here by standards.

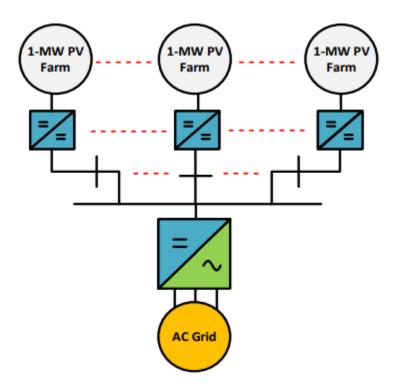


Figure 1: High-Power System Implementation for different local PV farm output aggregation

5. CONCLUSION

A prominent feature of the integration of AC or HVDC grids is DC-DC converters with a high transmission ratio. This article proposes four different topologies based on the cascading and serial relationships of the Cuk/SEPIC converter to fulfil these demands. For these converters, and for combined systems where a prototype for low power was used to tracks both constant load and consistent power scenarios for the predicted theoretical trends and performance, detailed theoretical models have been drawn up. The systems were also theoretically tested for a 1 MW PV application with high voltage levels over their active inputs equal to traditional isolated DC-DC converters for the specific high energy device.

REFERENCES

- [1].S. Aljoaba, A. Cramer, B. Walcott. "Thermoelectrical Modeling of Wavelength Effects on Photovoltaic Module Performance—Part I: Model". IEEE Journal of Photovoltaics. vol 3, No. 3, Jul 2013
- [2].J. Lee, B. Min, T. Kim, D. Yoo, J. Yoo. "A Novel Topology for Photovoltaic DC/DC Full-Bridge Converter with Flat Efficiency under Wide PV Module Voltage and Load Range". IEEE Transactions on Industrial Electronics, vol. 55, No. 7. Jul. 2008

ISSN- 2394-5125 VOL 7, ISSUE 19, 2020

- [3].E. Serban, M. Ordonez, C. Pondiche. "DC-Bus Voltage Range Extension in 1500 V Photovoltaic Inverters". IEEE Journal of Emerging & Selected Topics in Power Electronics, vol. 3, No. 4, Dec. 2015
- [4].R. Reiter, L. Michels, A. Peres, S. Oliveira. "Analysis of PV Arrays for Residential Applications Using a Three-Phase Step-Up Isolated DC-DC Converter with High-Frequency Transformer". 37th Annual Conference on IEEE Industrial Electronics Society. Nov. 2011
- [5].B. Williams. "DC-to-DC Converters with Continuous Input and Output Power". IEEE Transactions on Power Electronics. vol 28, No. 5. May 2013
- [6].Rathore, AK, Bhat, AK &Oruganti, R 2012, 'Analysis, design and experimental results of wide range ZVS active-clamped LL type current-fed DC/DC converter for fuel cells to utility interface', IEEE Transactions on Industrial Electronics, vol. 59, no. 1, pp. 473-485
- [7]. Yuan, B, Yang, X, Zeng, X, Duan, J, Zhai, J & Li, D 2012, 'Analysis and design of a high step-up current-fed multi resonant dc-dc converter with low circulating energy and zerocurrent switching for all active switches', IEEE Transactions on Industrial Electronics, vol. 59, no. 2, pp. 964-978
- [8]. Chen, Y & Kang, Y 2011, 'An Improved Full-bridge Dual-Output dc-dc Converter based on the Extended Complementary Pulse Width Modulation concept', IEEE Transactions on Power Electronics, vol. 26, no. 11, pp. 3215-3229.
- [9].Cho, SH, Kim, CS & Han, SK 2012, 'High-Efficiency and Low-cost tightly regulated dualoutput LLC resonant converter', IEEE Transactions on Industrial Electronics, vol. 59, no. 7, pp. 2982–2991
- [10]. Kim, HS, Jung, JH, Baek, JW & Kim, HJ 2013, 'Analysis and design of a multioutput converter using asymmetrical PWM half-bridge flyback converter employing a parallel-series transformer', IEEE Transactions on Industrial Electronics, vol. 60, no. 8, pp. 3115–3125.
- [11]. Singh, S, Bhuvaneswari, G & Singh, B 2010, 'Multiple output SMPS with improved input power quality', IEEE International Conference on Industrial and Information systems, pp.382–387.
- [12]. Liu, X, Xu, J, Chen, Z & Wang, N 2015, 'Single-Inductor Dual-Output Buck-Boost Power Factor Correction Converter', IEEE Transactions on Industrial Electronics, vol. 62, no. 2, pp. 943–952.